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NEWS 1 Web Page URLs for STN Seminar Schedule - N. America
NEWS 2 Dec 17 The CA Lexicon available in the CAPLUS and CA files
NEWS 3 Feb 06 Engineering Information Encompass files have new names
NEWS 4 Feb 16 TOXLINE no longer being updated
NEWS 5 Apr 23 Search Derwent WPINDEX by chemical structure
NEWS 6 Apr 23 PRE-1967 REFERENCES NOW SEARCHABLE IN CAPLUS AND CA
NEWS 7 May 07 DGENE Reload
NEWS 8 Jun 20 Published patent applications (A1) are now in USPATFULL

NEWS EXPRESS May 23 CURRENT WINDOWS VERSION IS V6.0a,
CURRENT MACINTOSH VERSION IS V5.0C (ENG) AND V5.0JB (JP),
AND CURRENT DISCOVER FILE IS DATED 06 APRIL 2001

NEWS HOURS STN Operating Hours Plus Help Desk Availability
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FILE 'HOME' ENTERED AT 14:03:56 ON 28 JUN 2001

=> file inspec compendex

| COST IN U.S. DOLLARS | SINCE FILE ENTRY | TOTAL SESSION |
|----------------------|------------------|---------------|
| FULL ESTIMATED COST | 0.15 | 0.15 |

FILE 'INSPEC' ENTERED AT 14:04:19 ON 28 JUN 2001

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FILE 'COMPENDEX' ENTERED AT 14:04:19 ON 28 JUN 2001

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=> s (updat#) and (module (w) block#)

L1 0 (UPDAT#) (MODULE (W) BLOCK#)

=> s (module (w) block#)

L2 25 (MODULE (W) BLOCK#)

=> s online (w) database (w) updat#

L3 0 ONLINE (W) DATABASE (W) UPDAT#

=> s database (w) updat#

L4 221 DATABASE (W) UPDAT#

=> l2 and l4

L2 IS NOT A RECOGNIZED COMMAND

The previous command name entered was not recognized by the system.
For a list of commands available to you in the current file, enter
"HELP COMMANDS" at an arrow prompt (=>).

=> s l2 and l4

L5 0 L2 AND L4

=> s l2 and updat#

L6 0 L2 AND UPDAT#

=> d l2 kwic 1-

YOU HAVE REQUESTED DATA FROM 25 ANSWERS - CONTINUE? Y/(N):n

=> focus l2

PROCESSING COMPLETED FOR L2

L7 25 FOCUS L2 1-

=> d kwic 1-10

L7 ANSWER 1 OF 25 INSPEC COPYRIGHT 2001 IEE

AB. . . given circuit so as to minimise the partitioning connections; the logical combination of the elements of a circuit into integrated **module blocks**; and the arrangement of integrated **module blocks** on a basis circuit board.

ST algorithm; **integrated module blocks**; PCD design; partitioning connection reduction; component arrangement

L7 ANSWER 2 OF 25 COMPENDEX COPYRIGHT 2001 EI

AB. . . of an automated stereo-warehouse. The data structure of the main files is given. The design principle is described by using the 'input' **module block** as an example. Finally, a flow chart of the input **module block** is given. 5 refs. In Chinese with English abstract.

L7 ANSWER 3 OF 25 INSPEC COPYRIGHT 2001 IEE

AB There are many methods of control, but they are generally classified as two types, **Module Block** Stack and Stored Program. In this paper, studies are described for getting the best machine control, in

the following sequence: . . .

ST foundry plant control system; **Module Block Stack**; Stored Program

L7 ANSWER 4 OF 25 INSPEC COPYRIGHT 2001 IEE

AB Refers to a thermoelectric **module block** assembly including a matrix of P-type and N-type thermoelectric elements joined by busbars to provide a matrix having a hot. . .

L7 ANSWER 5 OF 25 COMPENDEX COPYRIGHT 2001 EI

TI Twin **Module Block** (TMB) for rolling specialty steel wire rod.

ST Twin **module block** (TMB) concept

L7 ANSWER 6 OF 25 INSPEC COPYRIGHT 2001 IEE

AB A general discussion of production control and planning systems. A production network **module block** diagram is illustrated and conditions favouring outsourcing are examined. The authors note the need for awareness of the outside suppliers. . .

L7 ANSWER 7 OF 25 INSPEC COPYRIGHT 2001 IEE

AB. . . adaptation to the requirements of various medical applications, such as signal display, event detection or processing of results. System and **module block** diagrams are described and the overall system is open and adapted to a multiprocessor environment. Examples of signal processing are. . .

ST signal analysis system; biomedical signals; signal display; event detection; **module block diagrams**; multiprocessor environment; Fourier transforms

L7 ANSWER 8 OF 25 INSPEC COPYRIGHT 2001 IEE

AB. . . any heat to the semiconductor. The results of the analysis are verified experimentally and used in the case of a thyristor-**module -block** design.

ST heat conduction; semiconductor power device; **thyristor-module-block design**

L7 ANSWER 9 OF 25 INSPEC COPYRIGHT 2001 IEE

AB A wiring chip placed in an appropriate position on the **module blocks** out the desired bad octant or no octant if all good chips are utilized. By proper location of the wiring. . .

L7 ANSWER 10 OF 25 INSPEC COPYRIGHT 2001 IEE

AB To solve the problems of fault simulation and fault calculation, the fault in SOC is divided into **module blocks**. MUS (module under simulation) is extracted and SysFsim is designed and presented, which consists of two system level fault simulators: . . .

=> d kwic 11-

YOU HAVE REQUESTED DATA FROM 15 ANSWERS - CONTINUE? Y/(N):y

L7 ANSWER 11 OF 25 INSPEC COPYRIGHT 2001 IEE

AB In this paper, real-time power system voltage stability analysis software is introduced. The software includes the following **module blocks**: (1) interface with real-time state estimation; (2) load flow; (3) voltage stability criterion computation; (4) voltage stability limit computation; (5). . .

L7 ANSWER 12 OF 25 INSPEC COPYRIGHT 2001 IEE

AB. . . (Germany) university, dealing with a new concept for inverters. The inverter modules are string-connected and controlled by a single microprocessor **module**. **Block**, diagrams are shown and the following characteristics are specified: efficiency at least 93%, lower cost than separate inverters in the. . .

L7 ANSWER 13 OF 25 INSPEC COPYRIGHT 2001 IEE
 AB. . . processing is done by a Texas Instruments TMS320C31 processor, while the SMP bus system is based upon the miniKit 320C31 **module**. **Block** schematics are illustrated and the general architecture and operation of the system are described.

L7 ANSWER 14 OF 25 INSPEC COPYRIGHT 2001 IEE
 AB. . . are combined with timing data to an event word. Event words are stored in list mode in a VME memory **module**. **Blocks** of event words are scanned by transputers in VME and two-dimensional energy histograms are filled using the timing information to. . .

L7 ANSWER 15 OF 25 INSPEC COPYRIGHT 2001 IEE
 AB. . . low-power dual transceiver, a bus protocol unit, a memory controller with 1750 interface and a 128 kbit static RAM. A **module block** diagram is given and microprocessor connections are shown.

L7 ANSWER 16 OF 25 INSPEC COPYRIGHT 2001 IEE
 AB. . . is described. The logger has four analogue inputs with selectable 1 Hz/100 Hz sampling rate. Details of the data reduction **module-block** diagram and Pascal source code-and of the process of testing and integration are included.

L7 ANSWER 17 OF 25 INSPEC COPYRIGHT 2001 IEE
 AB. . . given. Using 'input' model block as an example, the design principle is described. Finally, a flow chart of the input **module block** is given.

L7 ANSWER 18 OF 25 COMPENDEX COPYRIGHT 2001 EI
 AB New technologies in the field of wire rod rolling - a bearing-free laying head, the Twin **Module Block** with high speed shear, latest design water cooling lines for temperature controlled rolling, process control and condition monitoring - make. . .

L7 ANSWER 19 OF 25 COMPENDEX COPYRIGHT 2001 EI
 AB. . . blade parts, such as curve fitting, surfaced modelling, curve and surface intersection, machining cutter path solution, and so on. The software **module block** for postposition process and simulator of cutter path was also exploited to control CNC machine by DNC mode in order. . .

L7 ANSWER 20 OF 25 COMPENDEX COPYRIGHT 2001 EI
 AB. . . are tabulated. Electromagnetic, switching and mechanical calculations demonstrated the possibility of implementing these parameters. It is pointed out that basically new **module blocks** were developed for these machines. They are: frame, collector, brushholder, ventilation unit. Each module is briefly described. A prototype of such machines. . .

L7 ANSWER 21 OF 25 COMPENDEX COPYRIGHT 2001 EI
 AB. . . distribution of signal and the qualitative properties of nonrandom false alarms are tabulated. A schematic diagram is presented of the detector **module block**. Sensor optimization and the signal-processing concept are also discussed. A design example of a InGaAs

array-silicon array is finally presented.16 Refs.

L7 ANSWER 22 OF 25 COMPENDEX COPYRIGHT 2001 EI

AB. . . single module to have several interfaces, and a single interface to be used for several different modules. Renaming, built-in modules, **module blocks**, and dynamic binding with views all further enhance the flexibility of reuse and rapid prototyping, which can be supported in. . .

L7 ANSWER 23 OF 25 COMPENDEX COPYRIGHT 2001 EI

AB. . . are combined with timing data to an Event word. Event words are stored in list mode in a VME memory **module**. **Blocks** of Event words are scanned by transputers in VME and two-dimensional energy histograms are filled using the timing information to. . .

L7 ANSWER 24 OF 25 COMPENDEX COPYRIGHT 2001 EI

AB. . . single-layer diamond drum based on a Ni bond for sizing and grinding of wood materials, and consisting of separate cylindrical **module blocks** has been developed. The special features of the drum design are described. The cutting conditions used and the results of experimental industrial. . .

L7 ANSWER 25 OF 25 COMPENDEX COPYRIGHT 2001 EI

ST 30 GHZ RECEIVER; ELECTRON BEAM LITHOGRAPHY; RECEIVER **MODULE BLOCK** DIAGRAM